



**MCA7K10 1600Gb/s to 2x800Gb/s,
OSFP to 2xOSFP, IHS to RHS Active
Copper Splitter Cable**

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1 Introduction

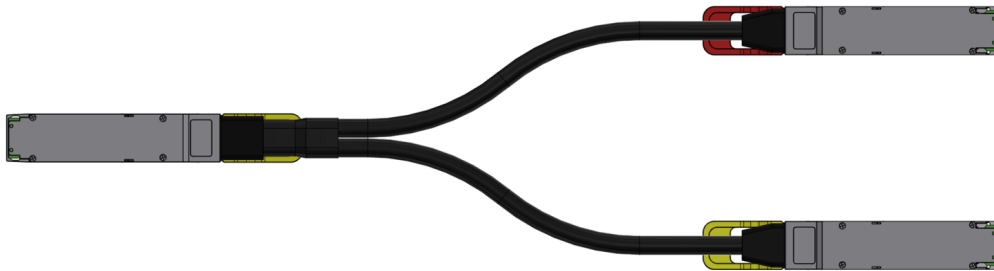
The NVIDIA® MCA7K10 is an 1600Gb/s twin-port OSFP (Octal Small Form-factor Pluggable) to 2x800Gb/s OSFP Active Copper Splitter Cable.

The cable uses 8-channels of 200G-PAM4 modulation and has lengths of up to 2-meters. The cable firmware supports both InfiniBand and Ethernet which are automatically enabled depending on the protocol of the switch. The 8-channel, twin-port, OSFP end uses a finned top form-factor for use in Quantum-3 switch cages. The two 800G ends support 4-channels of 200G-PAM4 (800G) use a flat top OSFP for use in ConnectX-8 adapters using riding heat sinks on the connector cage.

Each end includes an EEPROM which provides product identification and characteristics to the host system.

Main use is linking an 1600Gb/s Quantum-3 switch to OSFP-based 800Gb/s ConnectX-8 PCIe network adapter cards.

NVIDIA's cable solutions provide power-efficient connectivity enabling higher port bandwidth, density and configurability at a low cost and reduced power requirement in the data centers. Rigorous cable production testing ensures best out-of-the-box installation experience, performance, and durability.



Images are for illustration purposes only. Product labels, colors, and lengths may vary.

2 Key Features

- Up to 1600Gb/s split to two 800Gb/s data rates
- Based on 200G-PAM4 modulation
- OSFP switch end 4 Watts
- Single port OSFP ends 0.1 Watts
- Up to 2-meter lengths
- Operating case temperature 0-70°C
- Single 3.3V supply voltage
- Hot pluggable
- RoHS compliant
- LF (Lead Free) HF (Halogen Free) PCB
- [OSFPxmsa.org](https://www.OSFPxmsa.org) compliant
- CMIS compliant I²C management interface

3 Applications

- Quantum-3 InfiniBand switch-to-two 800Gb/s ConnectX-8 OSFP adapters (Only applicable to Q3200 and Q3401)

4 Pin Descriptions

The device is compliant with the Specification for OSFP (Octal Small Form Factor Pluggable) Modules.

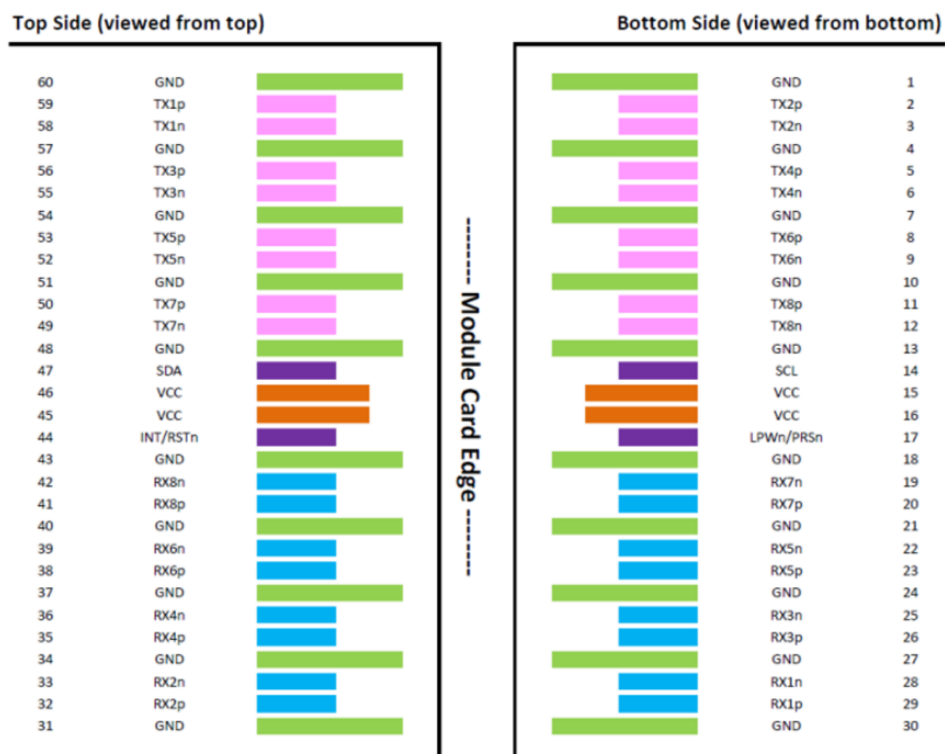
The pin assignment for the interface is shown below.

4.1 OSFP Pin Description

Pin	Symbol	Description	Pin	Symbol	Description
1	GND	Ground	31	GND	Ground
2	Tx2p	Transmitter Non-Inverted Data Input	32	Rx2p	Receiver Non-Inverted Data Output
3	Tx2n	Transmitter Inverted Data Input	33	Rx2n	Receiver Inverted Data Output
4	GND	Ground	34	GND	Grounds
5	Tx4p	Transmitter Non-Inverted Data Input	35	Rx4p	Receiver Non-Inverted Data Output
6	Tx4n	Transmitter Inverted Data Input	36	Rx4n	Receiver Inverted Data Output
7	GND	Ground	37	GND	Ground
8	Tx6p	Transmitter Non-Inverted Data Input	38	Rx6p	Receiver Non-Inverted Data Output
9	Tx6n	Transmitter Inverted Data Input	39	Rx6n	Receiver Inverted Data Output
10	GND	Ground	40	GND	Ground
11	Tx8p	Transmitter Non-Inverted Data Input	41	Rx8p	Receiver Non-Inverted Data Output
12	Tx8n	Transmitter Inverted Data Input	42	Rx8n	Receiver Inverted Data Output
13	GND	Ground	43	GND	Ground
14	SCL	2-wire serial interface clock	44	INT / RSTn	Module Interrupt / Module Reset
15	VCC	+3.3V Power	45	VCC	+3.3V Power
16	VCC	+3.3V Power	46	VCC	+3.3V Power
17	LPWn / PRSn	Low-Power Mode / Module Present	47	SDA	2-wire Serial interface data
18	GND	Ground	48	GND	Ground
19	Rx7n	Receiver Inverted Data Output	49	Tx7n	Transmitter Inverted Data Input
20	Rx7p	Receiver Non-Inverted Data Output	50	Tx7p	Transmitter Non-Inverted Data Input
21	GND	Ground	51	GND	Ground
22	Rx5n	Receiver Inverted Data Output	52	Tx5n	Transmitter Inverted Data Input
23	Rx5p	Receiver Non-Inverted Data Output	53	Tx5p	Transmitter Non-Inverted Data Input
24	GND	Ground	54	GND	Ground
25	Rx3n	Receiver Inverted Data Output	55	Tx3n	Transmitter Inverted Data Input

26	Rx3p	Receiver Non-Inverted Data Output	56	Tx3p	Transmitter Non-Inverted Data Input
27	GND	Ground	57	GND	Ground
28	Rx1n	Receiver Inverted Data Output	58	Tx1n	Transmitter Inverted Data Input
29	Rx1p	Receiver Non-Inverted Data Output	59	Tx1p	Transmitter Non-Inverted Data Input
30	GND	Ground	60	GND	Ground

4.1.1 OSFP Module Pad Layout



5 Specifications

5.1 Absolute Maximum Specifications

Absolute maximum ratings are those beyond which damage to the device may occur. Between the operational specifications and absolute maximum ratings, prolonged operation is not intended and permanent device degradation may occur.

Parameter	Min	Max	Max
Supply Voltage	-0.3	3.6	V
Data Input Voltage	-0.3	3.6	V
Control Input Voltage	-0.3	3.6	V

5.2 Environmental Specifications

This table shows the environmental specifications for the product.

Parameter	Min	Max	Units
Storage Temperature	-40	85	°C

5.3 Operational Specifications

This section shows the range of values for normal operation.

Parameter	Min	Typ	Max	Units
Supply Voltage (Vcc)	3.135	3.3	3.465	V
Power Consumption (1600G head end for the switch)	–	–	4.0	W
Power Consumption (800G tails for the HCA)	–	–	0.1	W
Operating Case Temperature	0		70	°C
Operating Relative Humidity	5		85	%

5.4 Electrical Specifications

Parameter	Min	Typ	Max	Units	Note
Characteristic impedance	90	95	100	Ω	
Time propagation delay	–	–	4.5	ns/m	Informative

5.5 Handling Precautions and Electrostatic Discharge (ESD)

The cable is compatible with ESD levels in typical data center operating environments and certified in accordance with the standards listed in the Regulatory Compliance Section. The product is shipped with protective caps on its connectors to protect it until the time of installation. In normal handling and operation of high-speed cables and optical transceivers, ESD is of concern during insertion into the OSFP cage of the server/switch. Hence, standard ESD handling precautions must be observed. These include use of grounded wrist/shoe straps and ESD floor wherever a cable/transceiver is extracted/inserted. Electrostatic discharges to the exterior of the host equipment chassis after installation are subject to system level ESD requirements.

5.6 Mechanical Specifications

Parameter	Value		Units
Diameter	Single ends: 9-10		mm
	Combined end: 14-15		
Length tolerance	length < 2 m	±25	mm
	length ≥ 2 m	±50	

5.6.1 Minimum Bend Radius

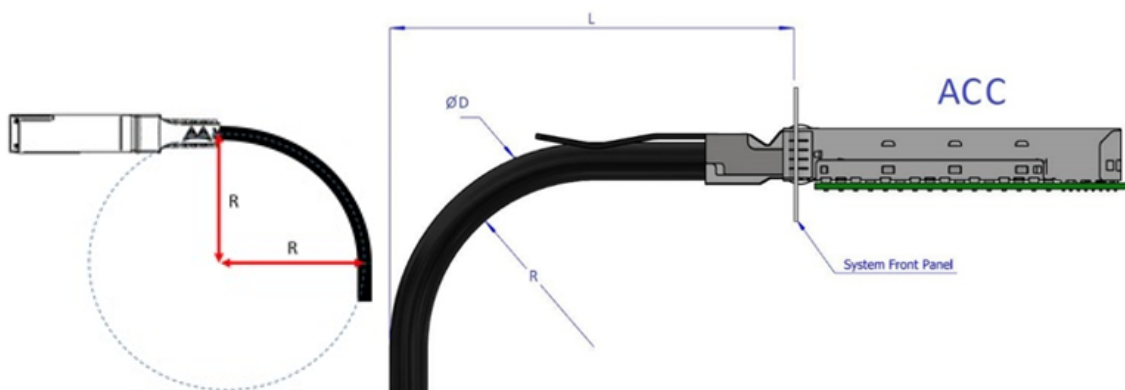
OPN	Length (m)	AWG (mm)	Cable Diameter Single Ends	Cable Diameter Combined End	Min bend radius R (mm) Single Ends	Min bend radius R (mm) Combined End	Assembly Space L* Combined/Single End (mm)*
MCA 7K10	up to 2	26AWG, 2x8pairs	9-10	14-15	90	100	145/100

The minimum assembly bending radius (close to the connector) is 10x the cable's outer diameter. The repeated bend (far from the connector) is also 10x the cable's outer diameter. The single bend (far from the connector) is 5x the cable's outer diameter.

***'Combined' end is the 'head' where the cables join together, inserted into the switch. 'Single' end is the 'tail' which plugs into the HCA/NIC in a server.

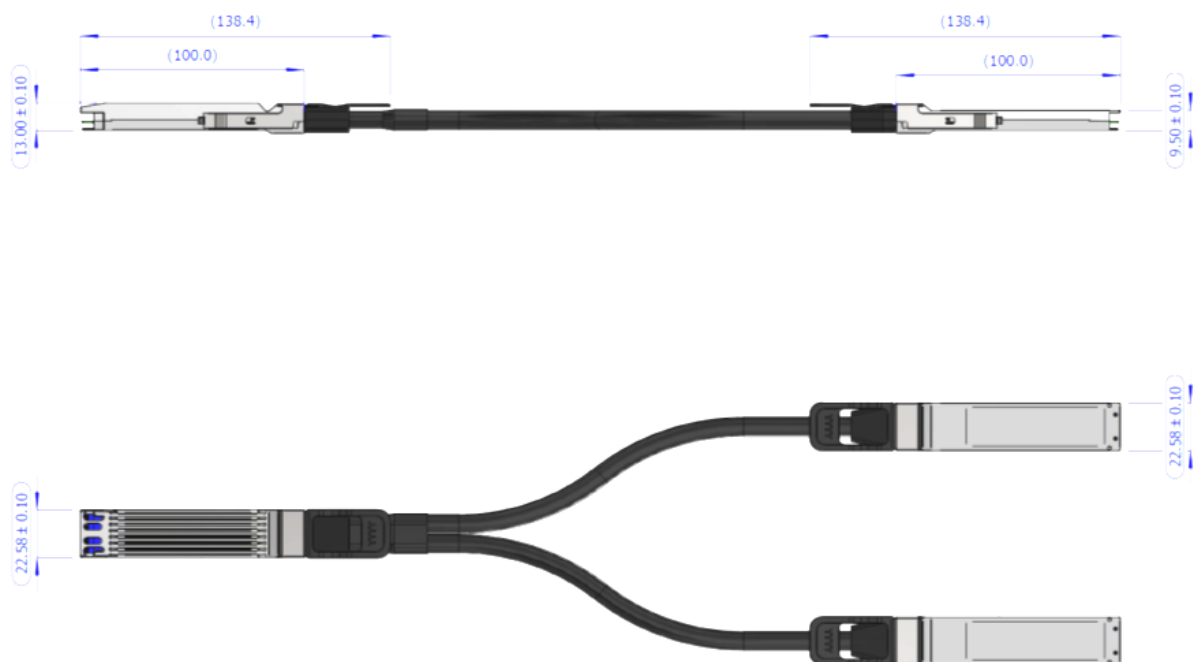
L = Assembly Space. Minimum value depends on the backshell (connector housing) dimensions = the space for the cable assembly behind the rack door.

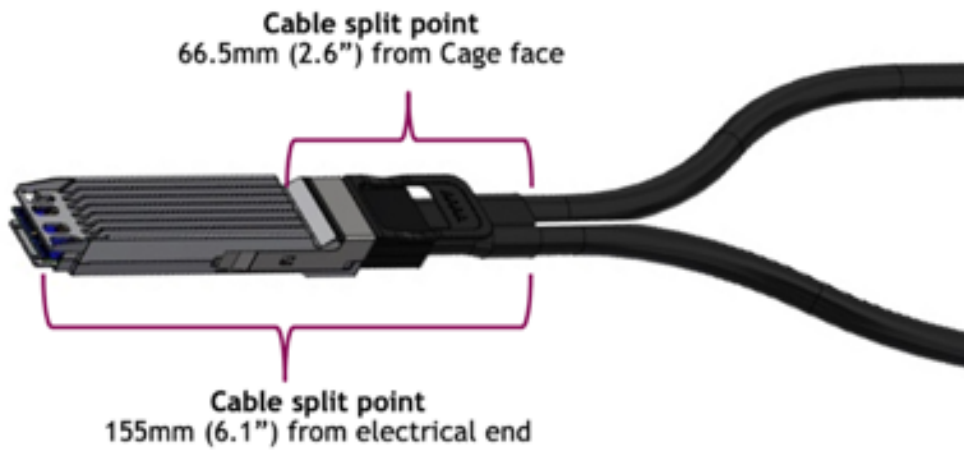
5.6.2 Assembly Bending Radius



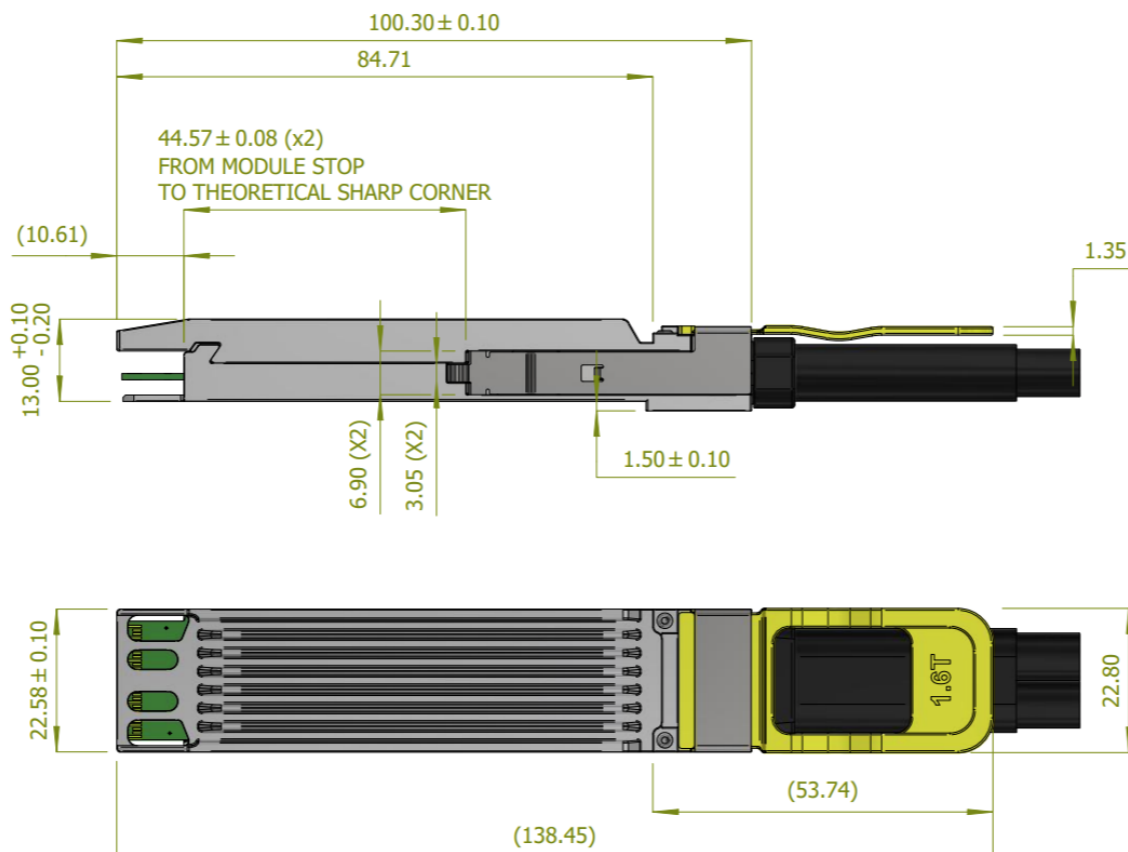
5.6.3 Mechanical Drawings

5.6.3.1 Dimensions

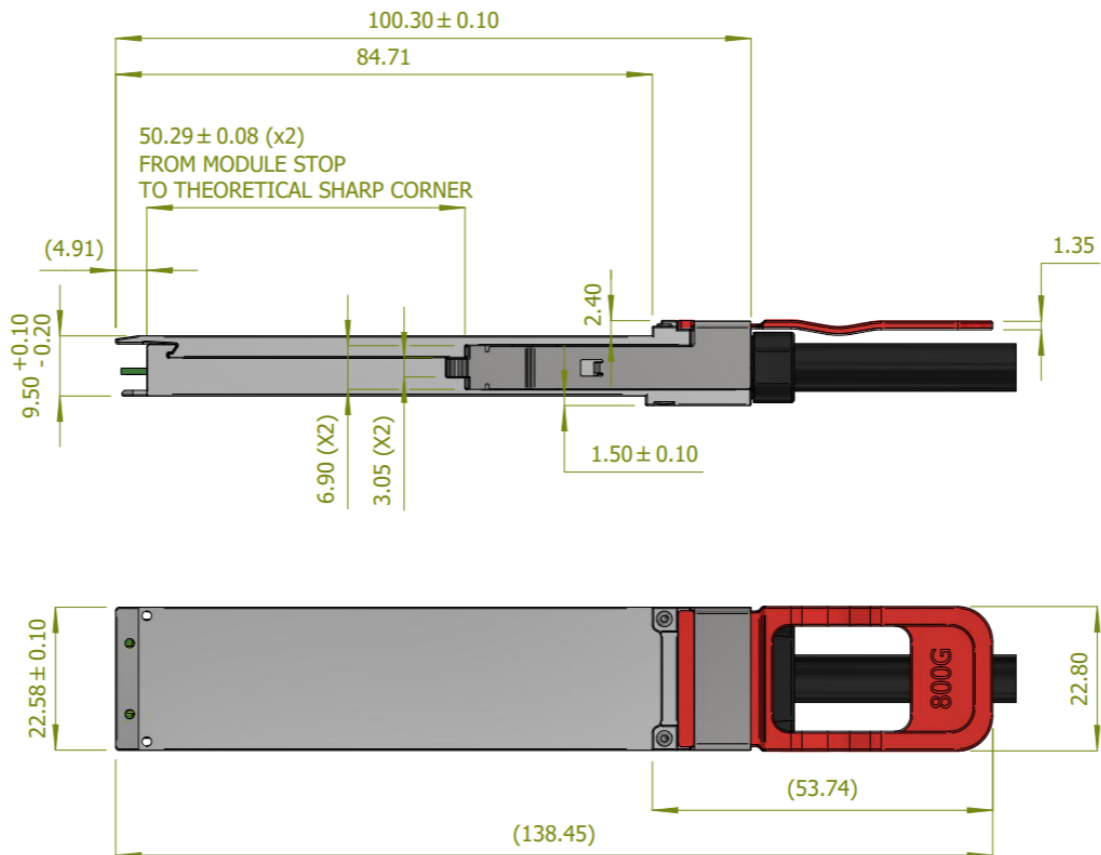




5.6.3.2 Finned Head Dimensions



5.6.3.3 Flat Ends Dimensions



5.7 Labels

5.7.1 Backshell Label

The following label is applied on the cable's backshell. Note that the images are for illustration purposes only. Labels look and placement may vary.

OSFP switch end	OSFP split ends
-----------------	-----------------

Model No: XXXXXX PN: XXXXXXXX-XXXXX SN: MTYYWWXXSSSSS Rev: ZZ Xm XXAWG YYYY-MM-DD 800Gb/s Made In COO	 NVIDIA	Model No: XXXXXX PN: XXXXXXXX-XXXXX SN: MTYYWWXXSSSSS Rev: ZZ Xm XXAWG YYYY-MM-DD 800Gb/s Made In COO	 NVIDIA
Images are for illustration purposes only. Product labels, colors, and form may vary.		(sample illustration)	

5.7.1.1 Backshell Label Legend

Symbol	Meaning	Notes
PN – Part Number		
xx	Length	Meters
SN – Serial Number		
MT	Manufacturer name	2 characters
YY	Year of manufacturing	2 digits
WW	Week of manufacturing	2 digits
SSSSS	Serial number	5 digits for serial number. Reset at start of week to 00001.
Miscellaneous		
ZZ	HW and SW revision	2 alpha-numeric characters
Xm	Cable length	Meters
XXAWG	Cable gauge	American wire gauge
YYYY-MM-DD	Year-month-day	Year 4 digits, month 2 digits, day 2 digits
COO	Country of origin	E.g., China
	Quick response code	Serial number

5.7.2 Cable Jacket Label (Middle of Cable)

The following label is applied on the cable's jacket. Note that the images are for illustration purposes only. Labels look and placement may vary.



(sample illustration)

The serial number and barcode are for NVIDIA internal use only. Images are for illustration purposes only. Product labels, colors, and form may vary.

5.8 Regulatory Compliance and Classification

- Safety: CB, TUV, CE, EAC, UKCA
- EMC: CE, FCC, ICES, RCM, VCCI

Ask your NVIDIA FAE for a zip file of the certifications for this product.

5.9 FCC Class A Notice

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.



6 Ordering Information

Legacy OPN	NVIDIA OPN	Description
MCA7K10	980-9IA05-00X001	NVIDIA Active copper splitter cable, 1600Gb/s to 2x800Gb/s, OSFP to 2xOSFP, 1m, IHS to RHS
	980-9IA05-00X01A	NVIDIA Active copper splitter cable, 1600Gb/s to 2x800Gb/s, OSFP to 2xOSFP, 1.5m, IHS to RHS
	980-9IA05-00X002	NVIDIA Active copper splitter cable, 1600Gb/s to 2x800Gb/s, OSFP to 2xOSFP, 2m, IHS to RHS

7 Document Revision History

Revision	Date	Description of Changes
1.0	Jan. 2026	First public release.

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